



US 20030164902A1

(19) **United States**(12) **Patent Application Publication** (10) Pub. No.: **US 2003/0164902 A1****Dawson et al.**(43) Pub. Date: **Sep. 4, 2003**(54) **FLAT MOLDABLE HDTV DISPLAY**(21) Appl. No.: **09/844,840**

(76) Inventors: **Thomas Patrick Dawson**, Escondido, CA (US); **David Bessel**, Poway, CA (US); **David Boyden**, Escondido, CA (US); **David Alan Desch**, Poway, CA (US); **Paul Georgief**, San Diego, CA (US); **Priyan Deveka Gunatilake**, San Diego, CA (US); **Kevin Jones**, San Diego, CA (US); **Takaaki Ota**, San Diego, CA (US); **Christopher Jensen Read**, San Diego, CA (US); **Kenichi Kawasaki**, San Diego, CA (US)

(22) Filed: **Apr. 27, 2001****Publication Classification**(51) Int. Cl.⁷ **H04N 3/14; H04N 9/30**(52) U.S. Cl. **348/790; 348/791**(57) **ABSTRACT****Correspondence Address:**

BLAKELY SOKOLOFF TAYLOR & ZAFMAN
12400 WILSHIRE BOULEVARD, SEVENTH
FLOOR
LOS ANGELES, CA 90025 (US)

An apparatus comprising a plurality of columns bound together, configured to display a single row of pixels scanned out from top to bottom. It also comprises a timing control unit to synchronize between the columns to allow all pixels of a single row to be lit at the same time.

